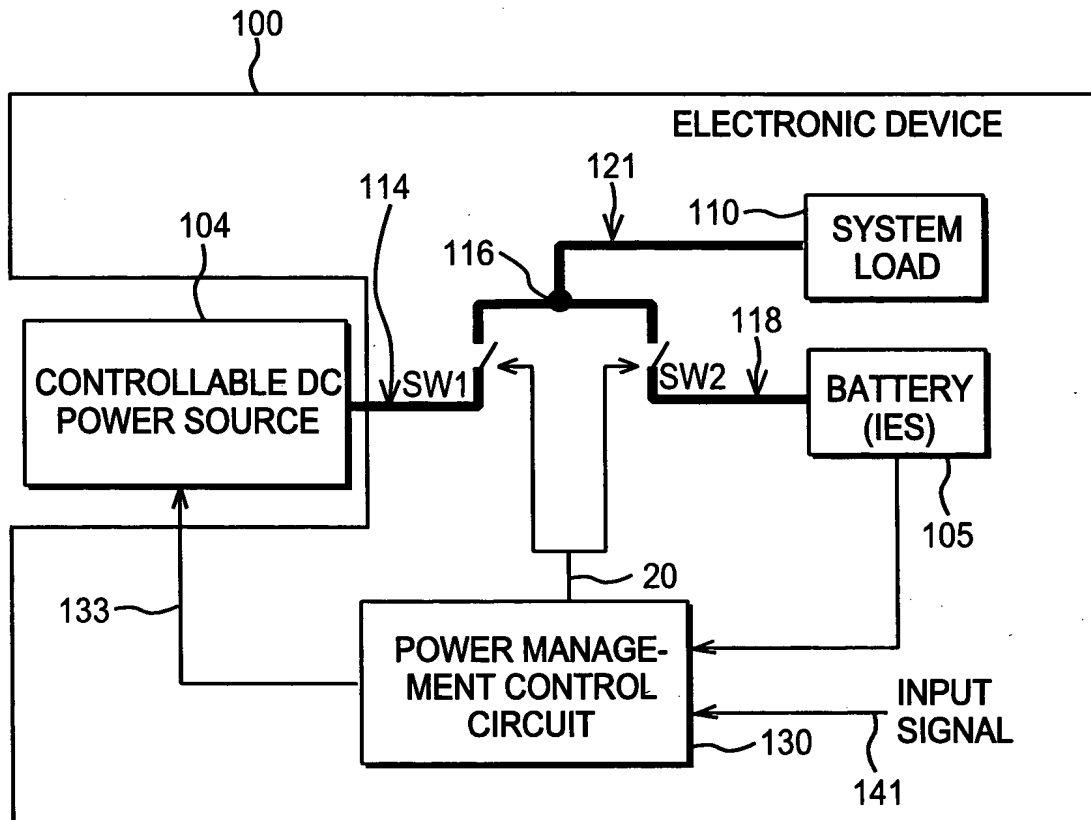


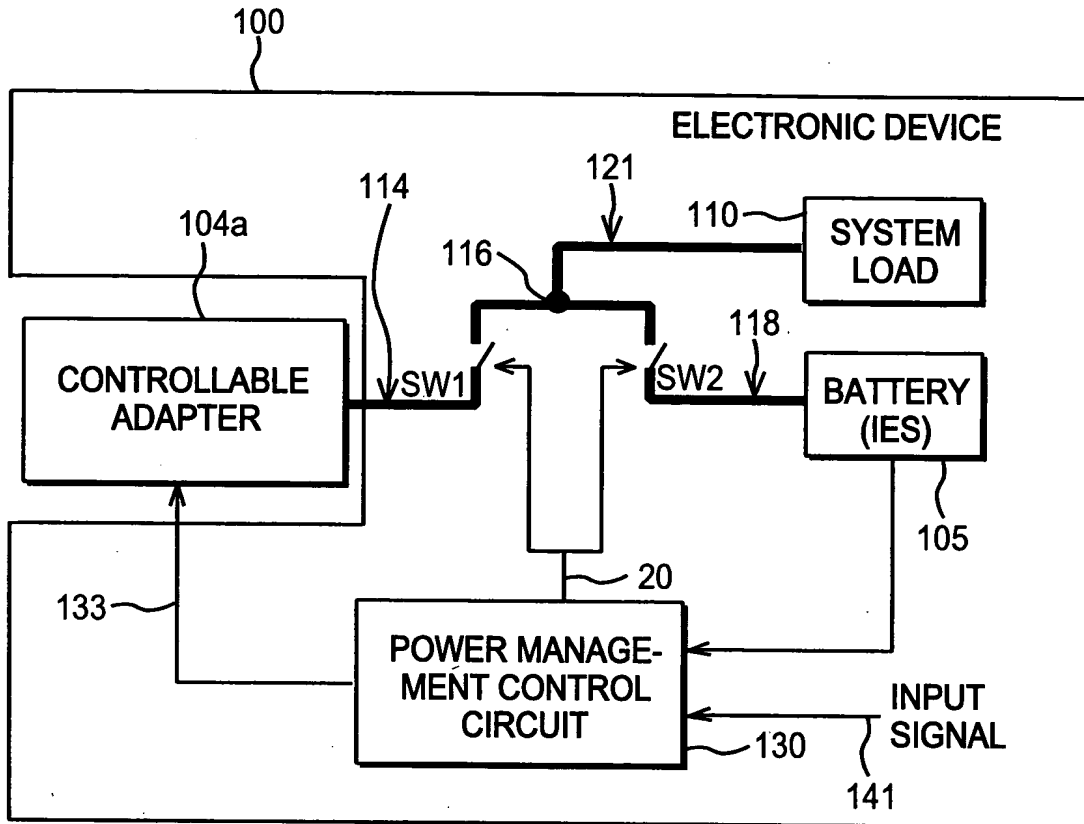
# FIG. 1



SUPPLY MODE	SW1	SW2
POWER BY DC NO CHARGE	ON ↑	OFF
POWER BY DC CHARGE	ON ↑	ON ↓
BUFFER BATTERY	ON ↑	ON ↑
POWER BY BATTERY	OFF	ON ↑

180  
 181  
 183  
 185

# FIG. 2



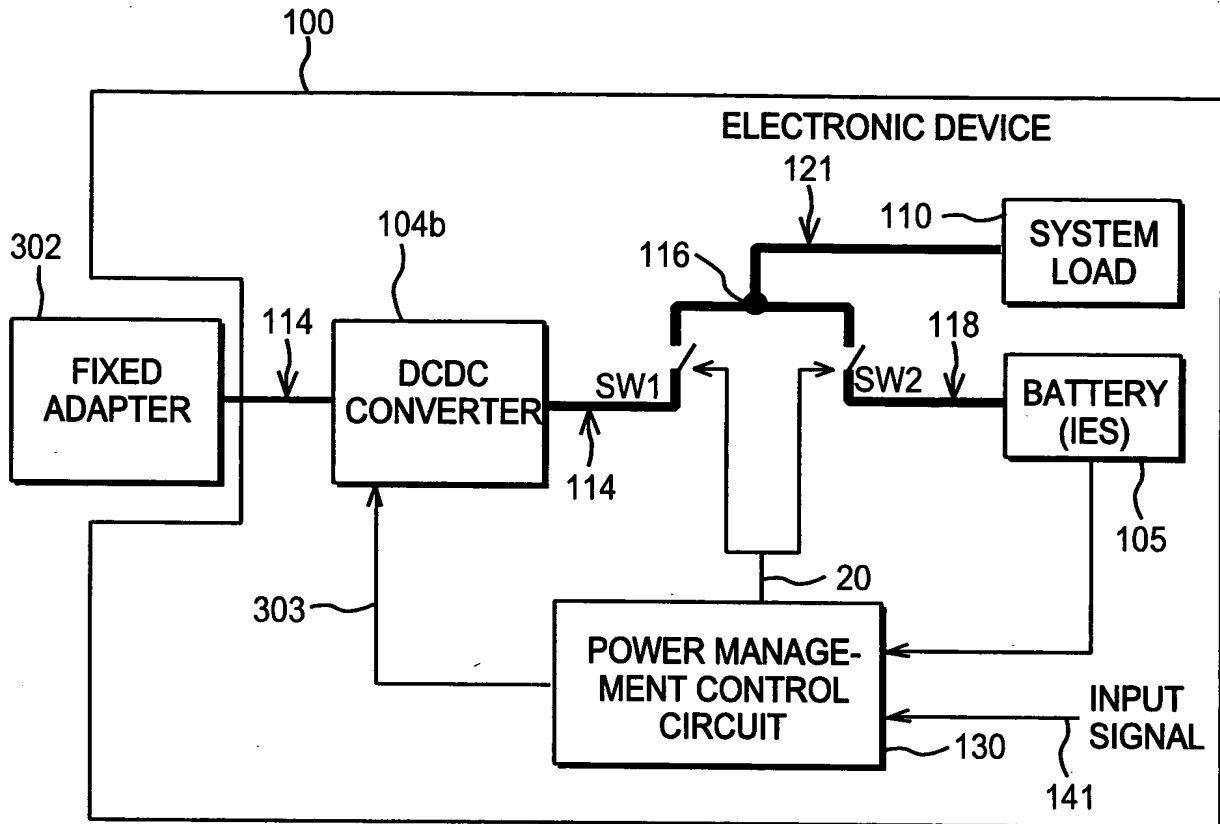
180

SUPPLY MODE	SW1	SW2
POWER BY DC NO CHARGE	ON ↑	OFF
POWER BY DC CHARGE	ON ↑	ON ↓
BUFFER BATTERY	ON ↑	ON ↑
POWER BY BATTERY	OFF	ON ↑

190

SUPPLY MODE	SW1	SW2
POWER BY DC NO CHARGE	ON ↑	OFF
POWER BY DC CHARGE	ON ↑	ON ↓
BUFFER BATTERY	NOT AVAILABLE	
POWER BY BATTERY	OFF	ON ↑

# FIG. 3



SUPPLY MODE	SW1	SW2
POWER BY DC NO CHARGE	ON↑	OFF
POWER BY DC CHARGE	ON↑	ON↓
BUFFER BATTERY	ON↑	ON↑
POWER BY BATTERY	OFF	ON↑

180

185

FIG. 4

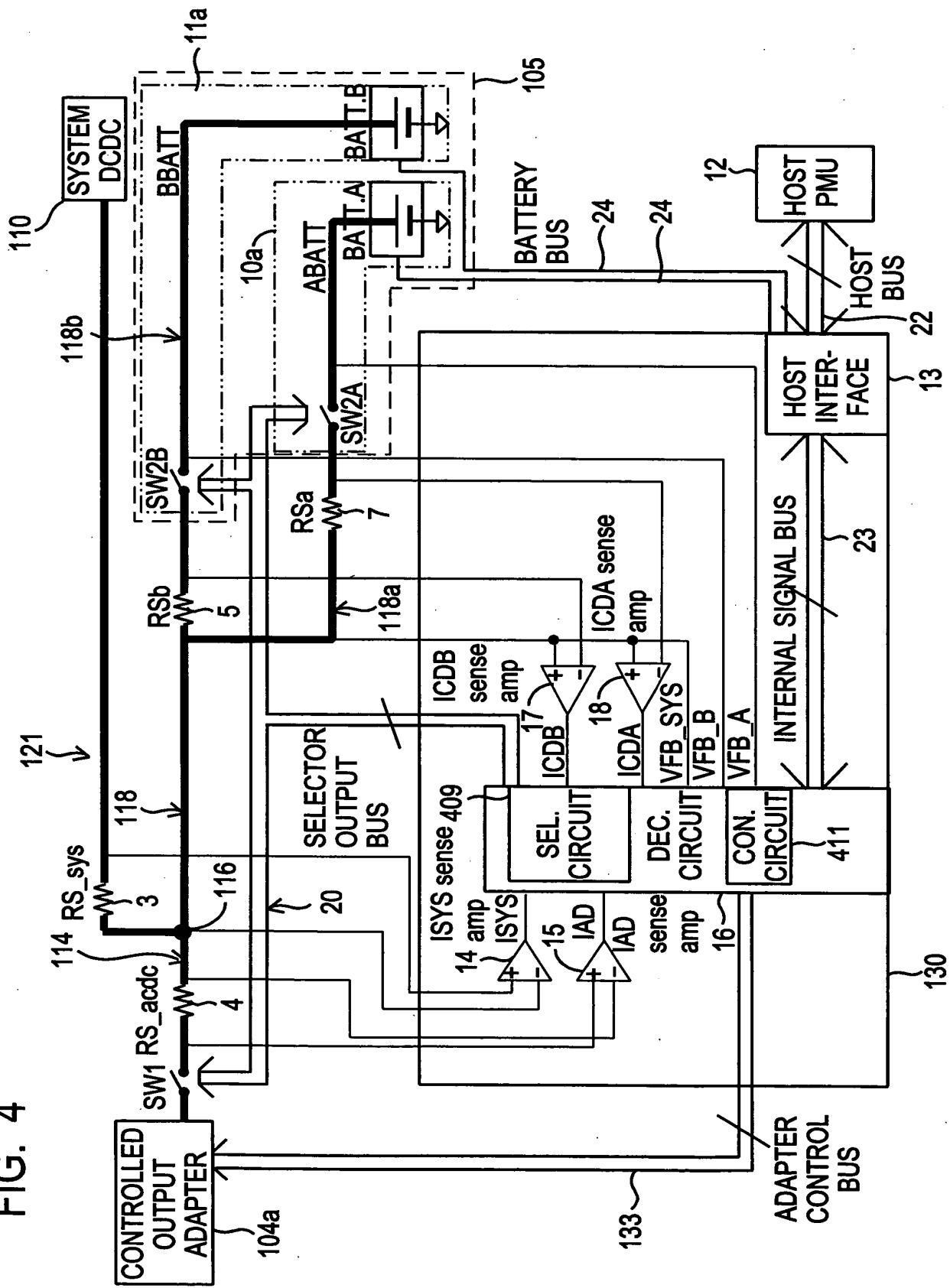


FIG. 5

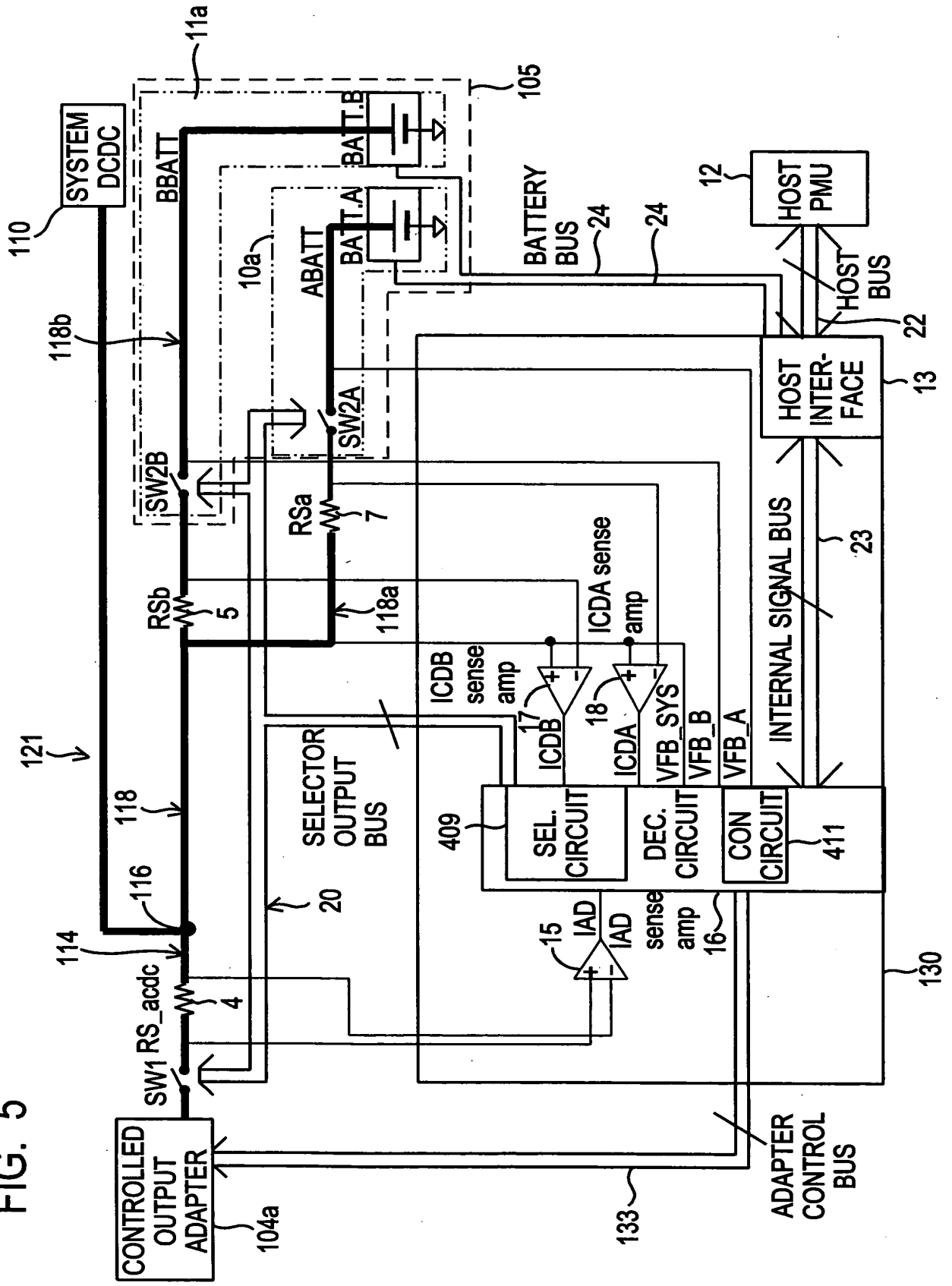


FIG. 6

The diagram illustrates a power management system (FIG. 6) with the following components and connections:

- Controlled Output Adapter (104a):** Connected to the system DCDC (110) and the selector output bus (20) via switch SW1.
- System DCDC (110):** Provides power to the system, connected to the selector output bus (20) via resistor RS\_sys (3).
- Selector Output Bus (20):** A central bus connecting the adapter, DCDC, and battery bus. It includes resistors RSb (5) and RSa (7).
- Battery Bus (105):** Connected to the selector output bus (20) via switch SW2B and resistor RSb (5). It also includes a battery (10a) and a capacitor (118b).
- Decoder Circuit (16):** Receives signals from the selector output bus (20) and provides feedback to the system DCDC (110) via switch SW2A and resistor RSa (7). It includes sense amplifiers (14, 17, 18) and feedback loops (VFB\_SYS, VFB\_A, VFB\_B).
- Host PMU (12):** Manages the system, connected to the host interface (13) and the adapter control bus (133).
- Host Interface (13):** Connects the host PMU (12) to the internal signal bus (23).
- Adapter Control Bus (133):** Connects the adapter (104a) to the host PMU (12) via the host interface (13).
- Internal Signal Bus (23):** Connects the host interface (13) to the decoder circuit (16).
- Capacitors (114, 116, 118, 118a, 118b):** Used for filtering and stability in the power lines.

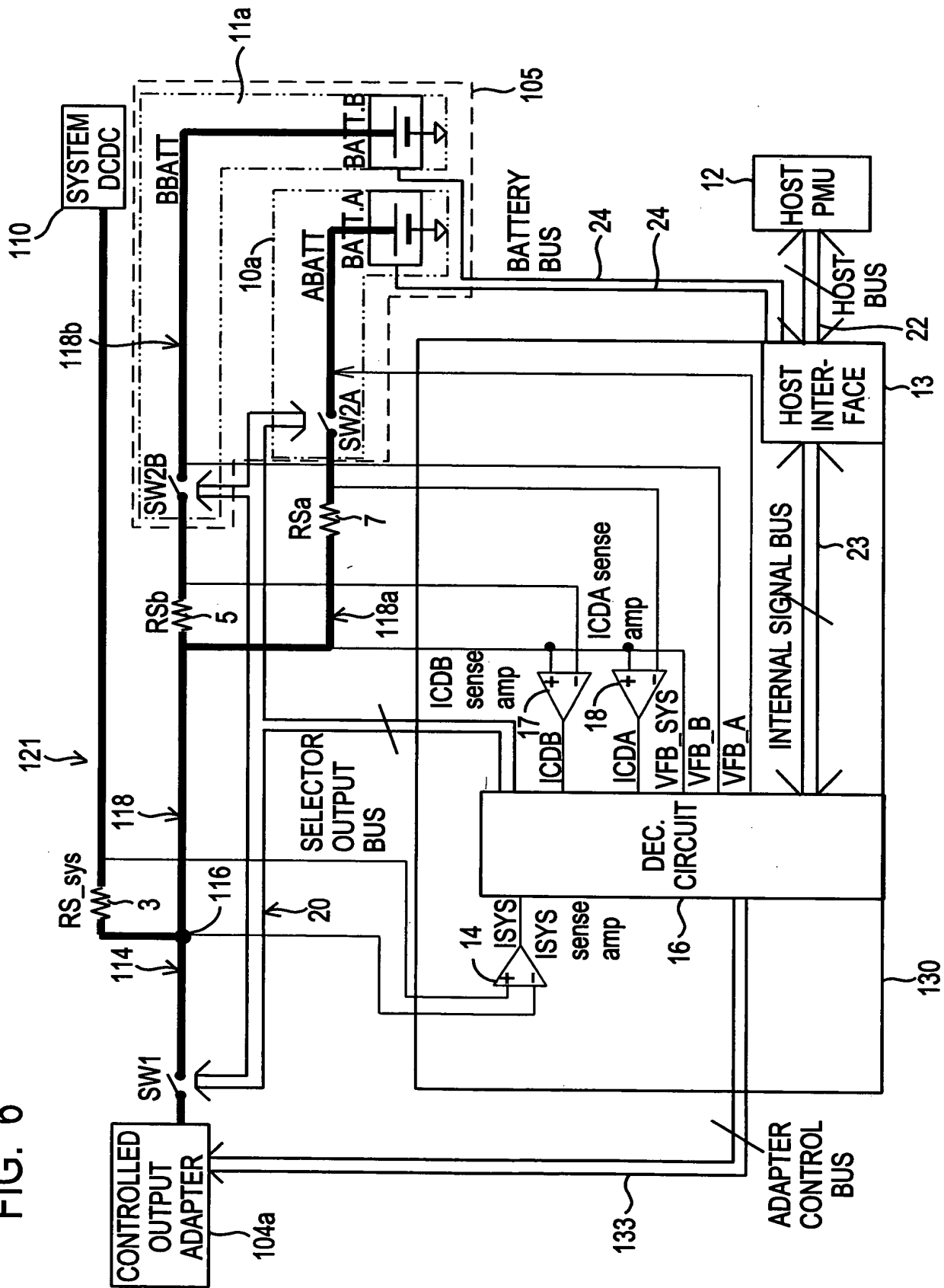


FIG. 7

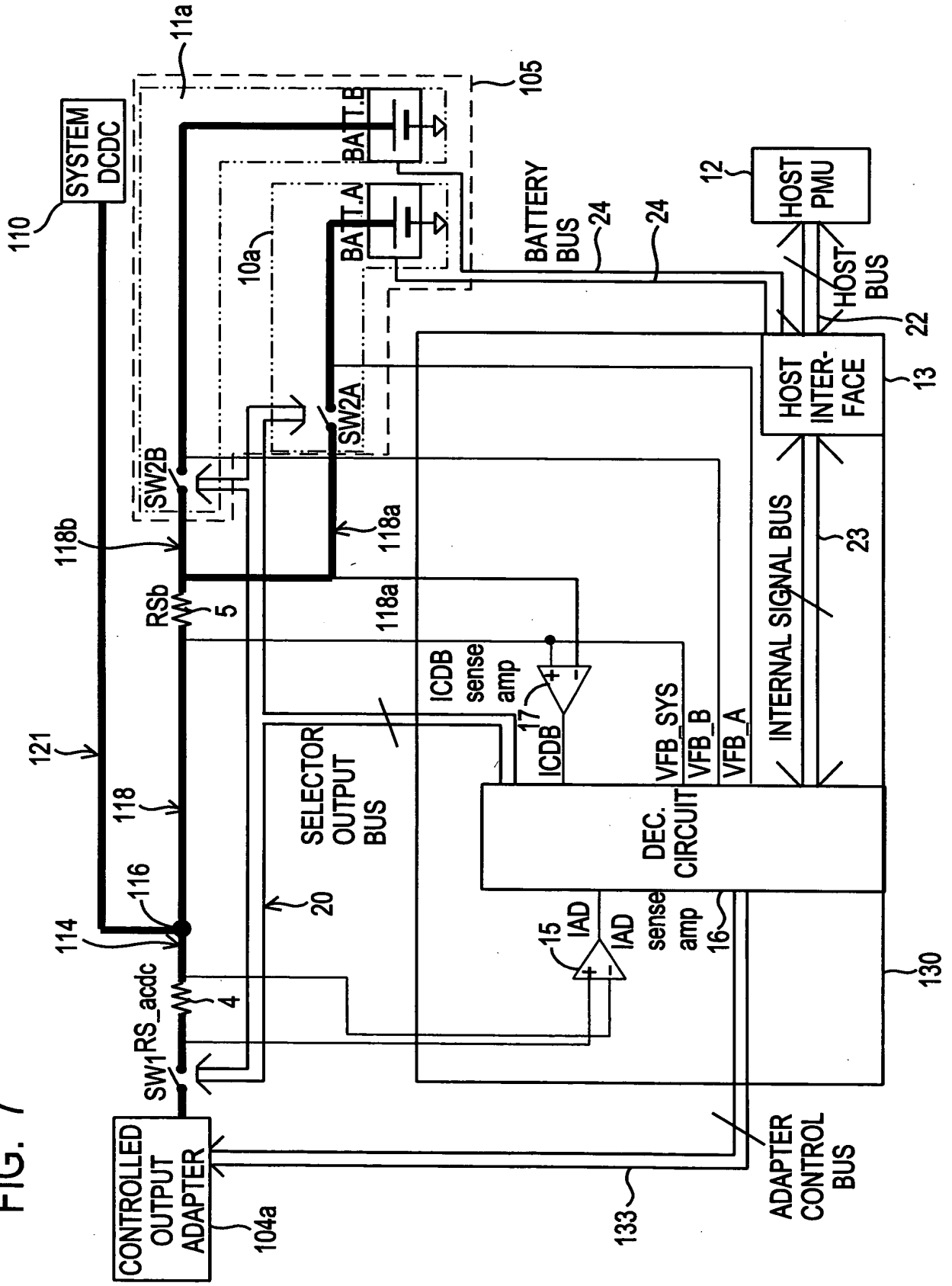


FIG. 8

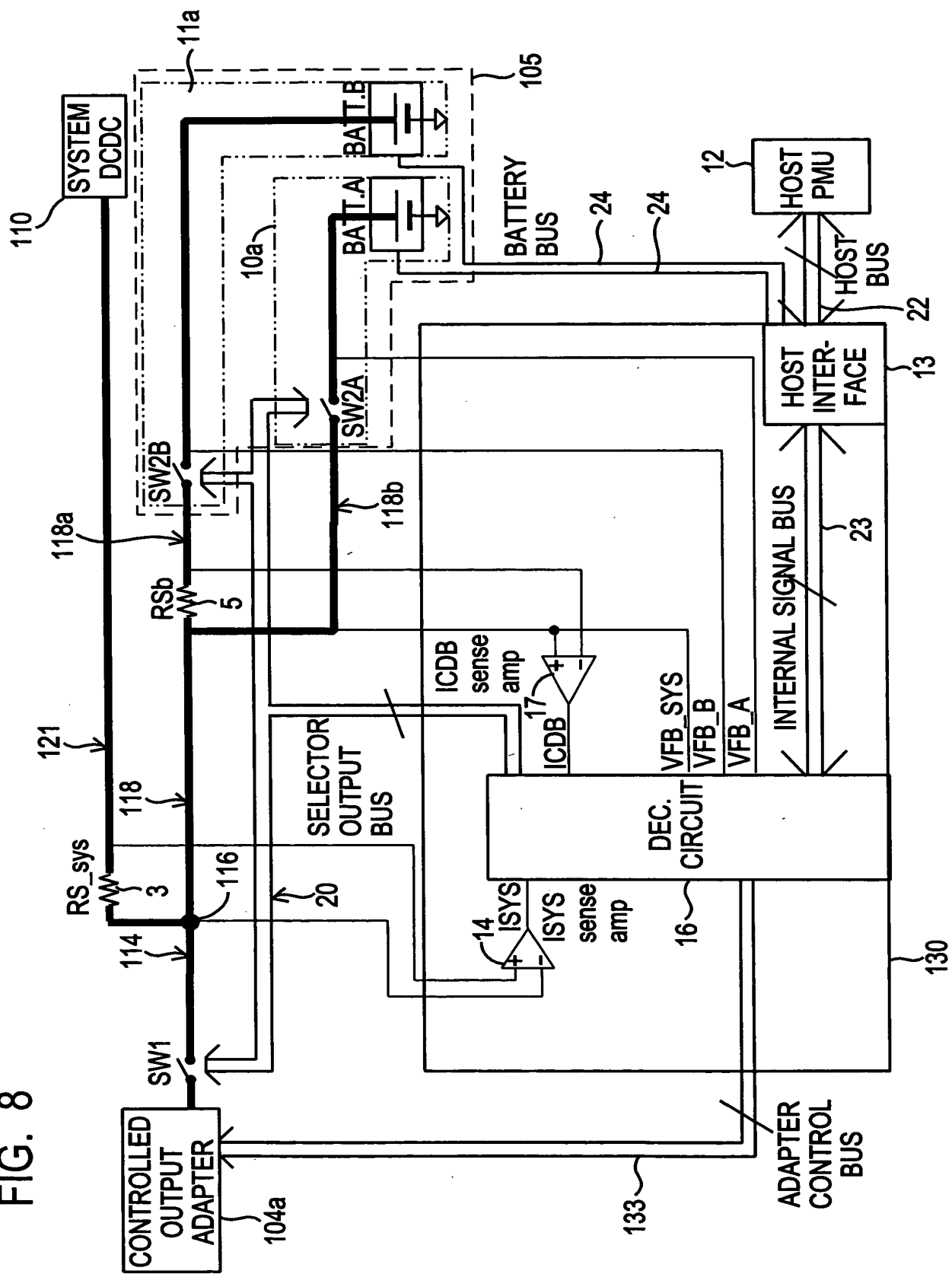


FIG. 9

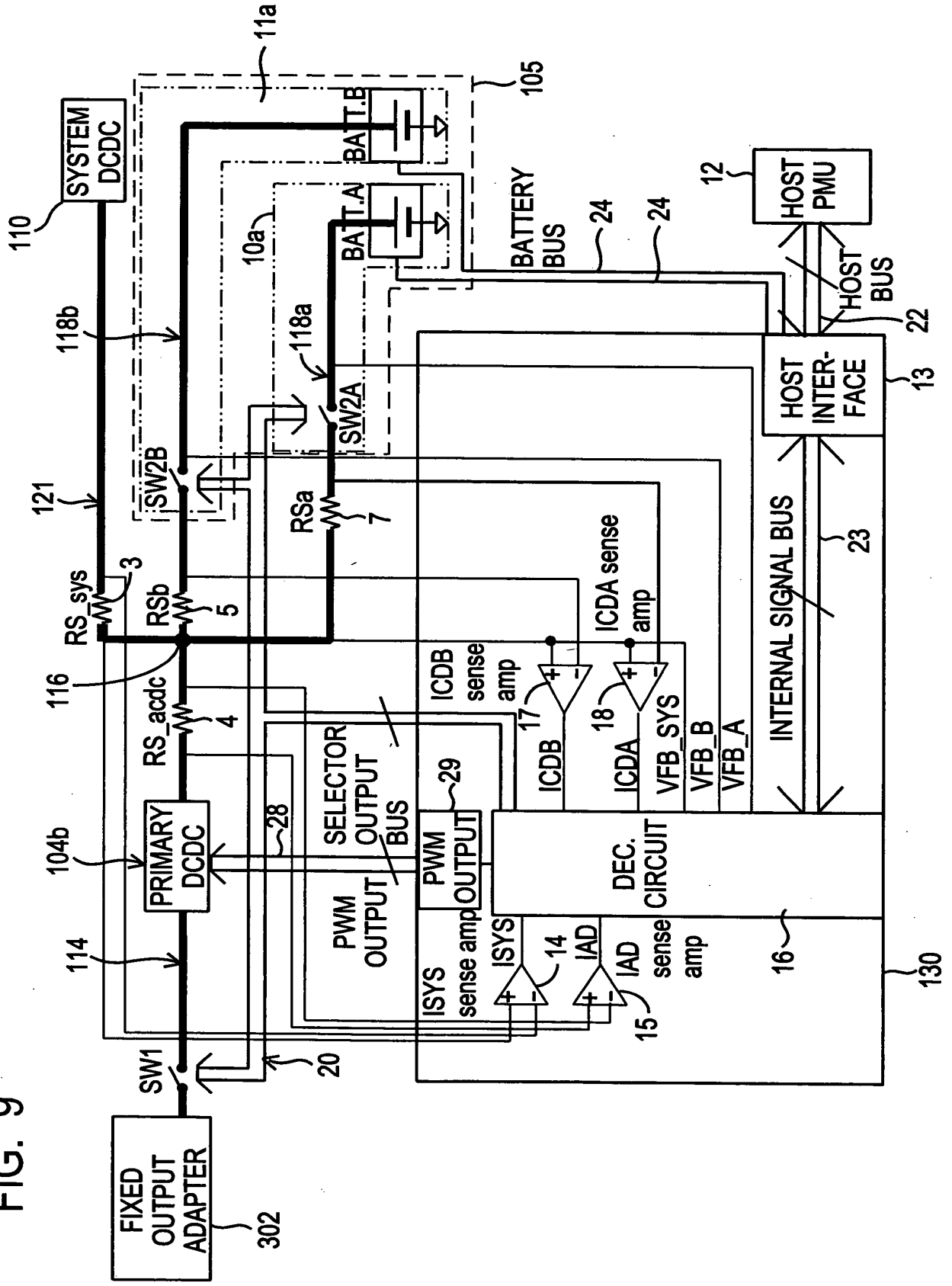


FIG. 10

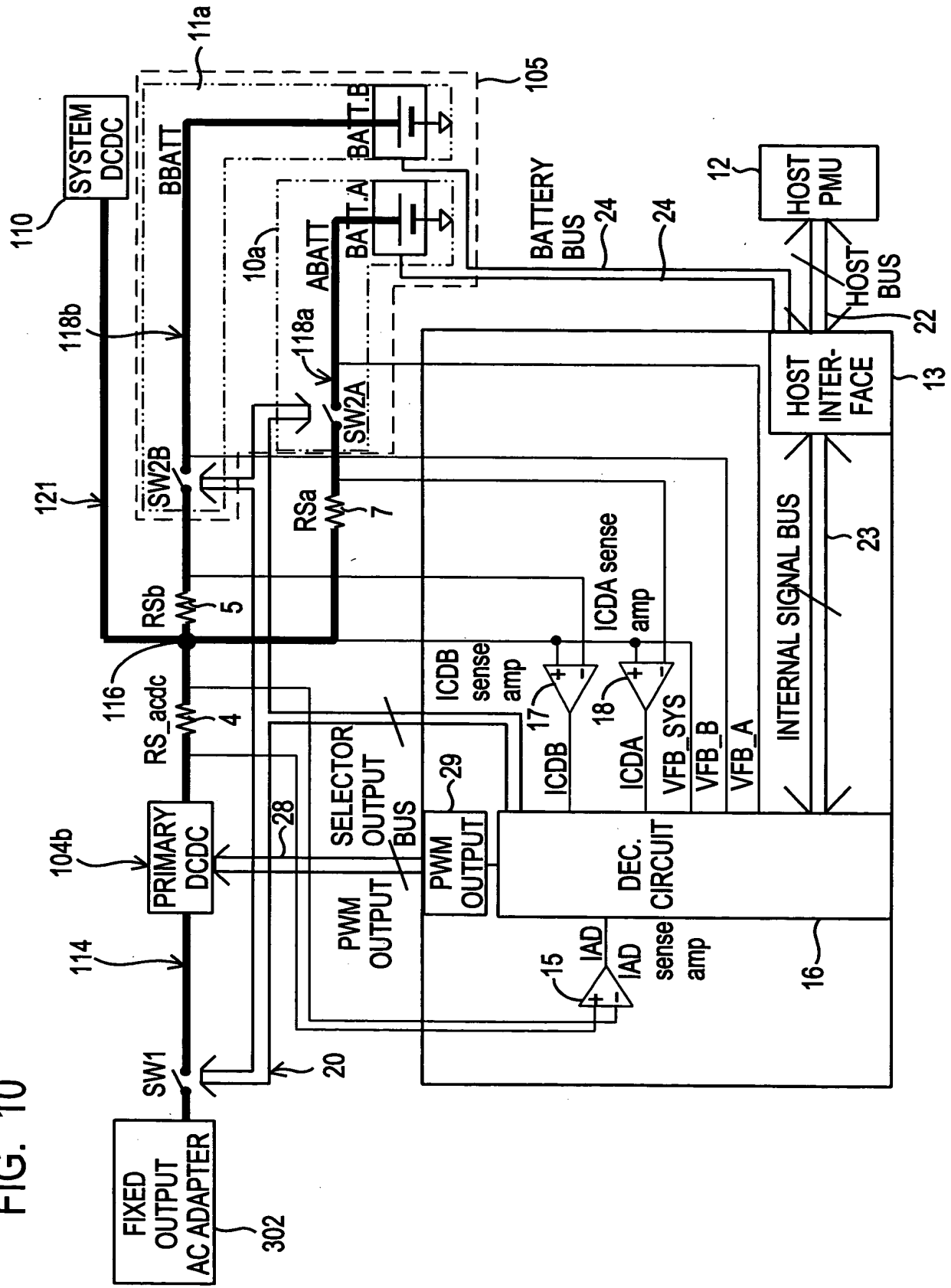


FIG. 11

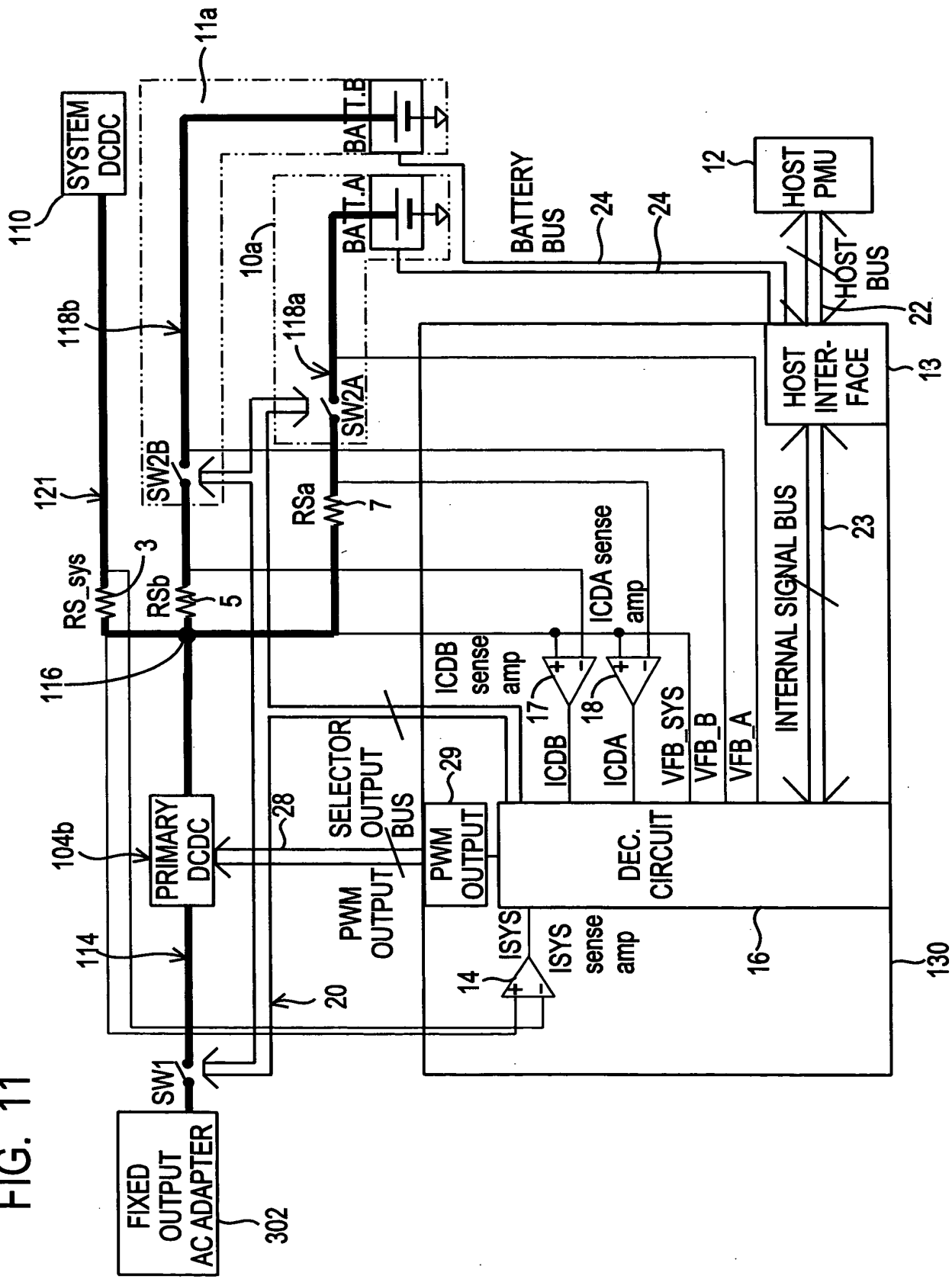


FIG. 12

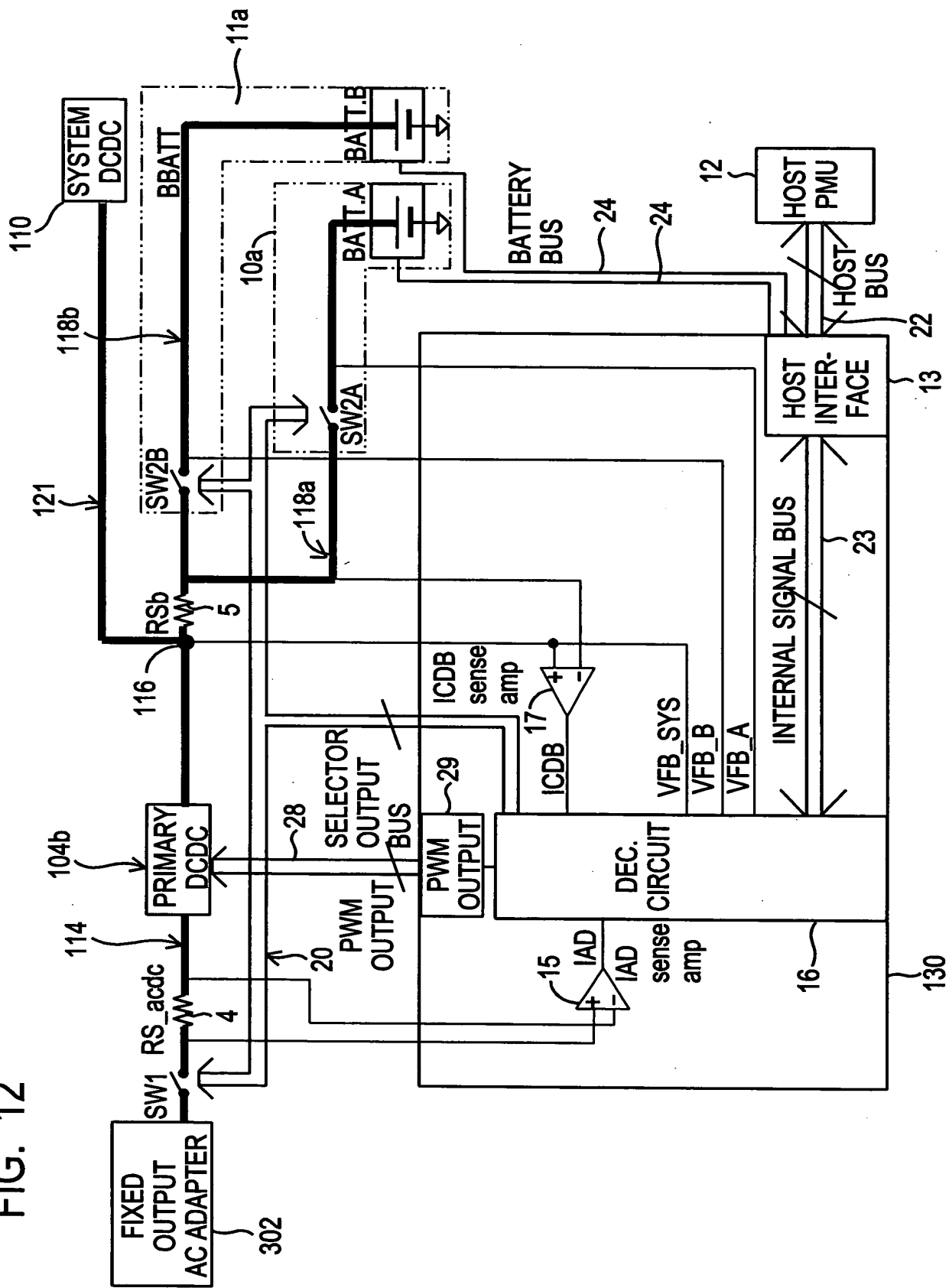


FIG. 13

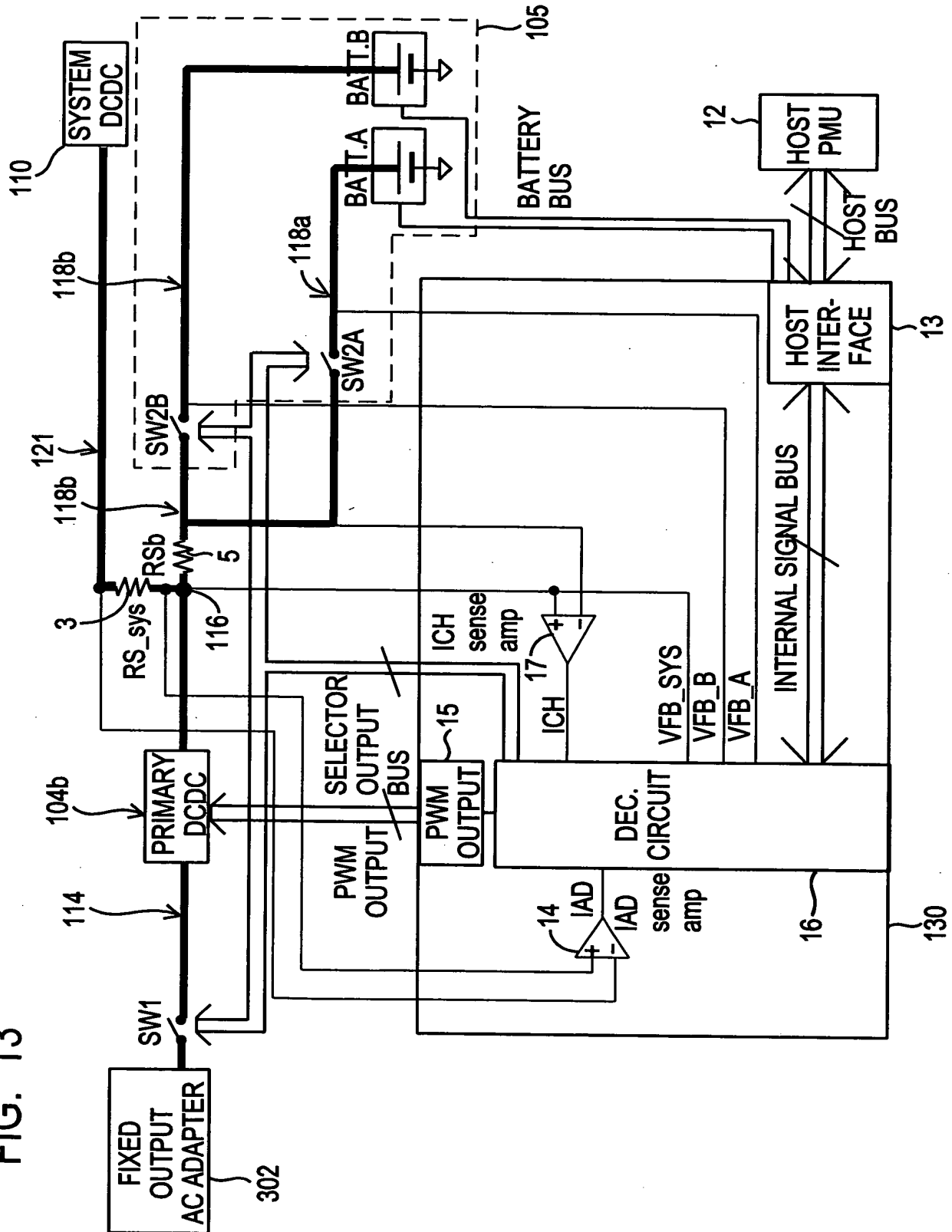


FIG. 14

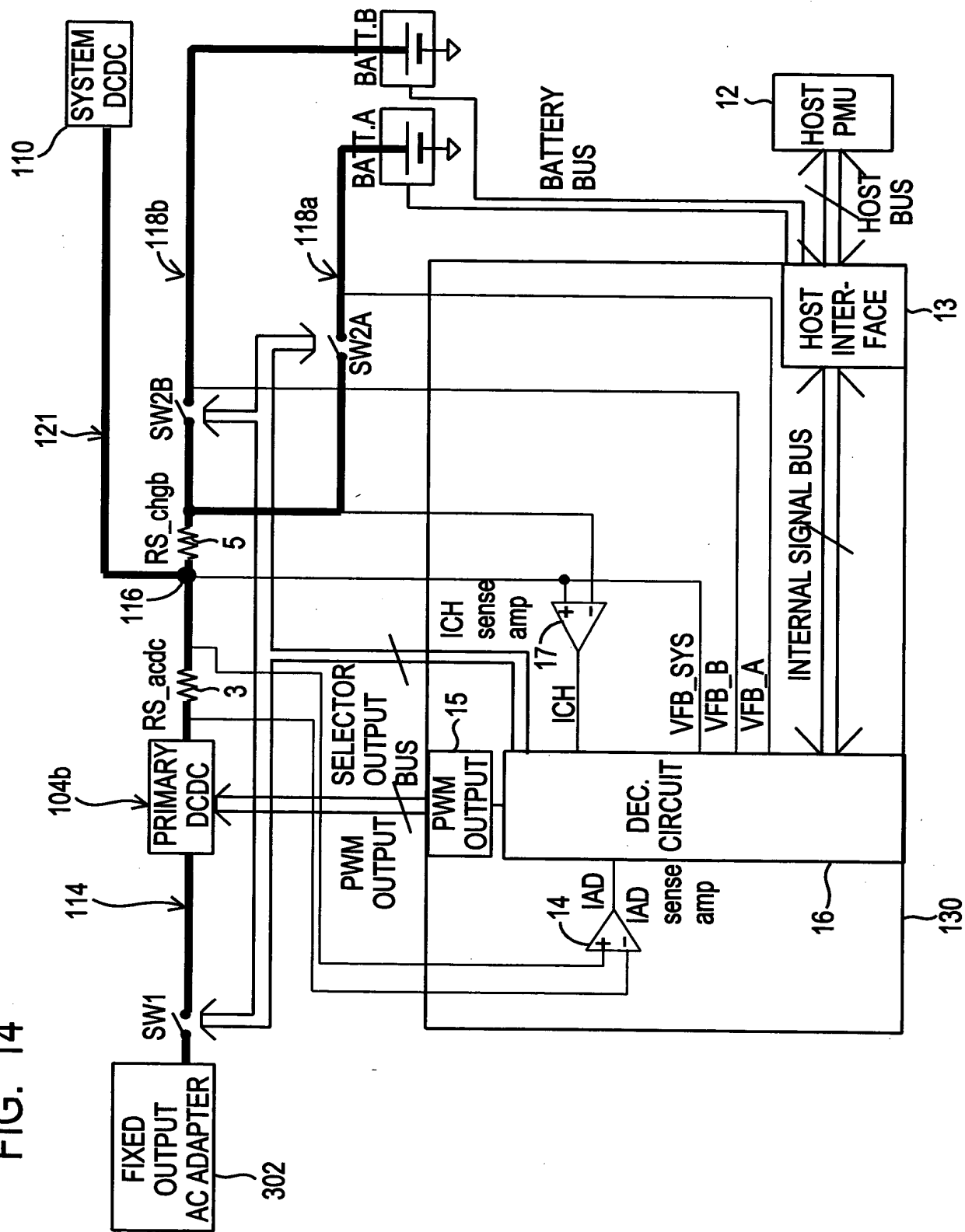
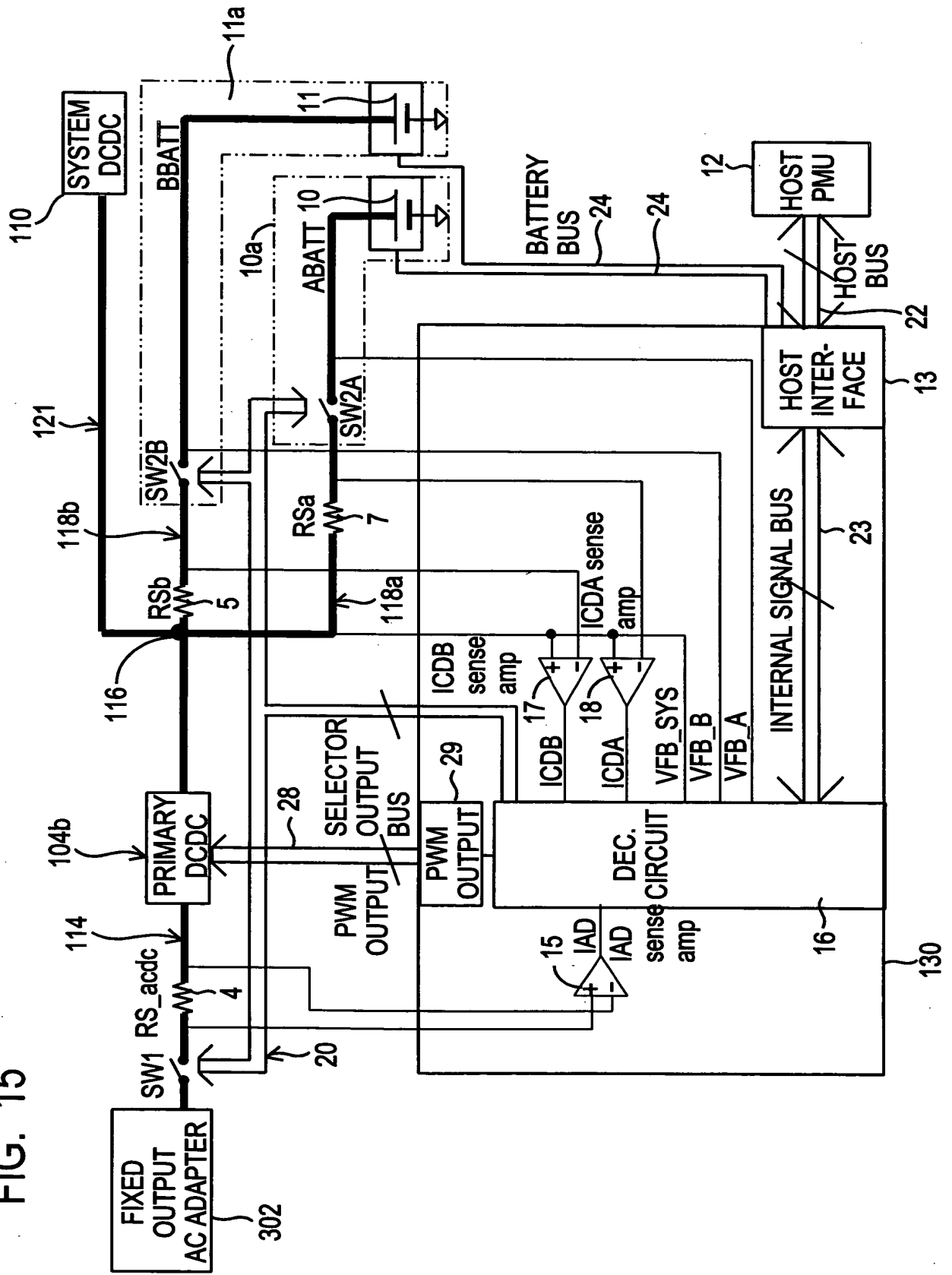
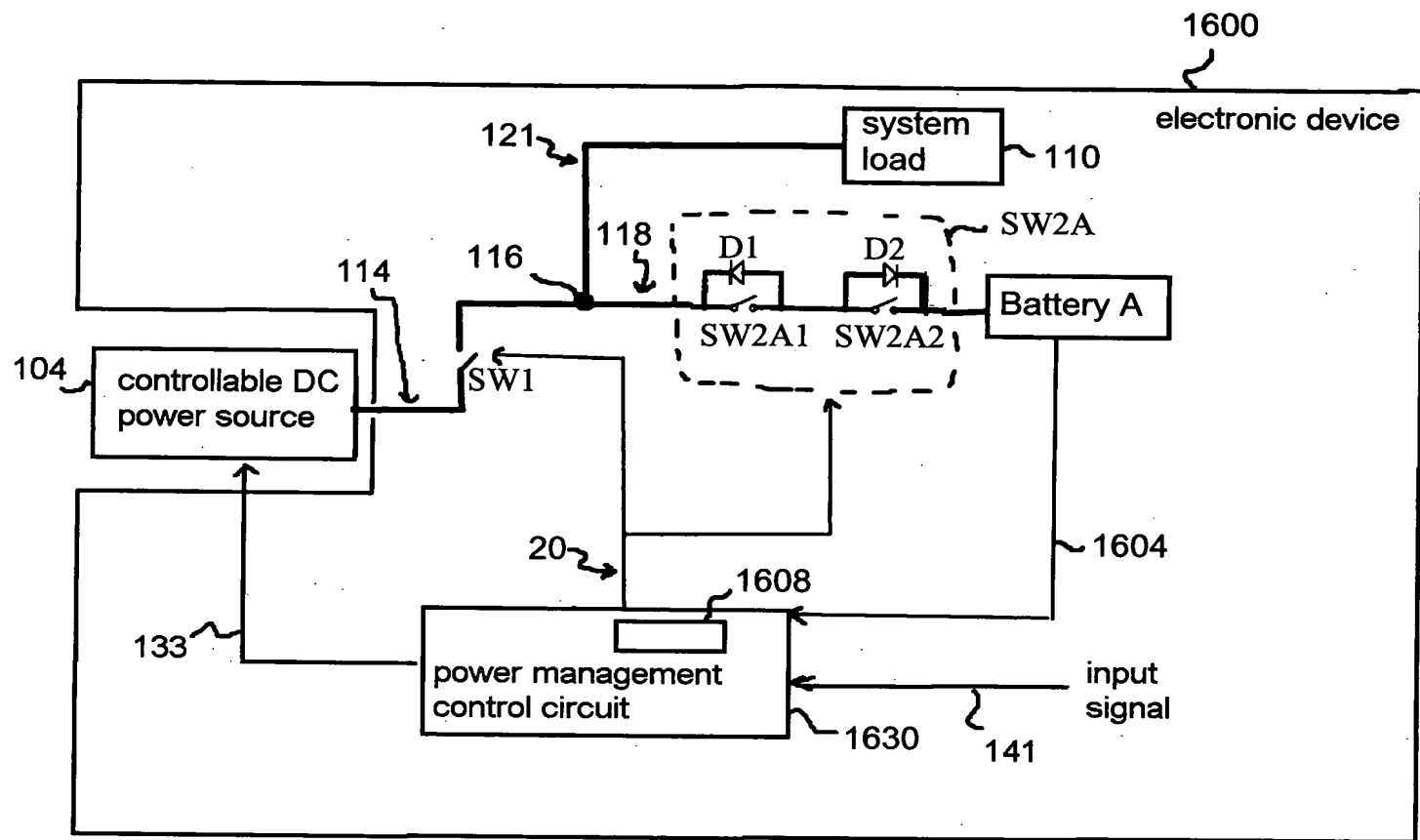


FIG. 15





1680

		SW2A	
Supply mode		SW2A1	SW2A2
181 Power by dc no charge	ON	OFF	OFF
183 Power by dc charge	ON	ON	OFF
185 Buffer battery	ON	OFF	ON
187 Power by battery	OFF	ON	ON

FIG. 16

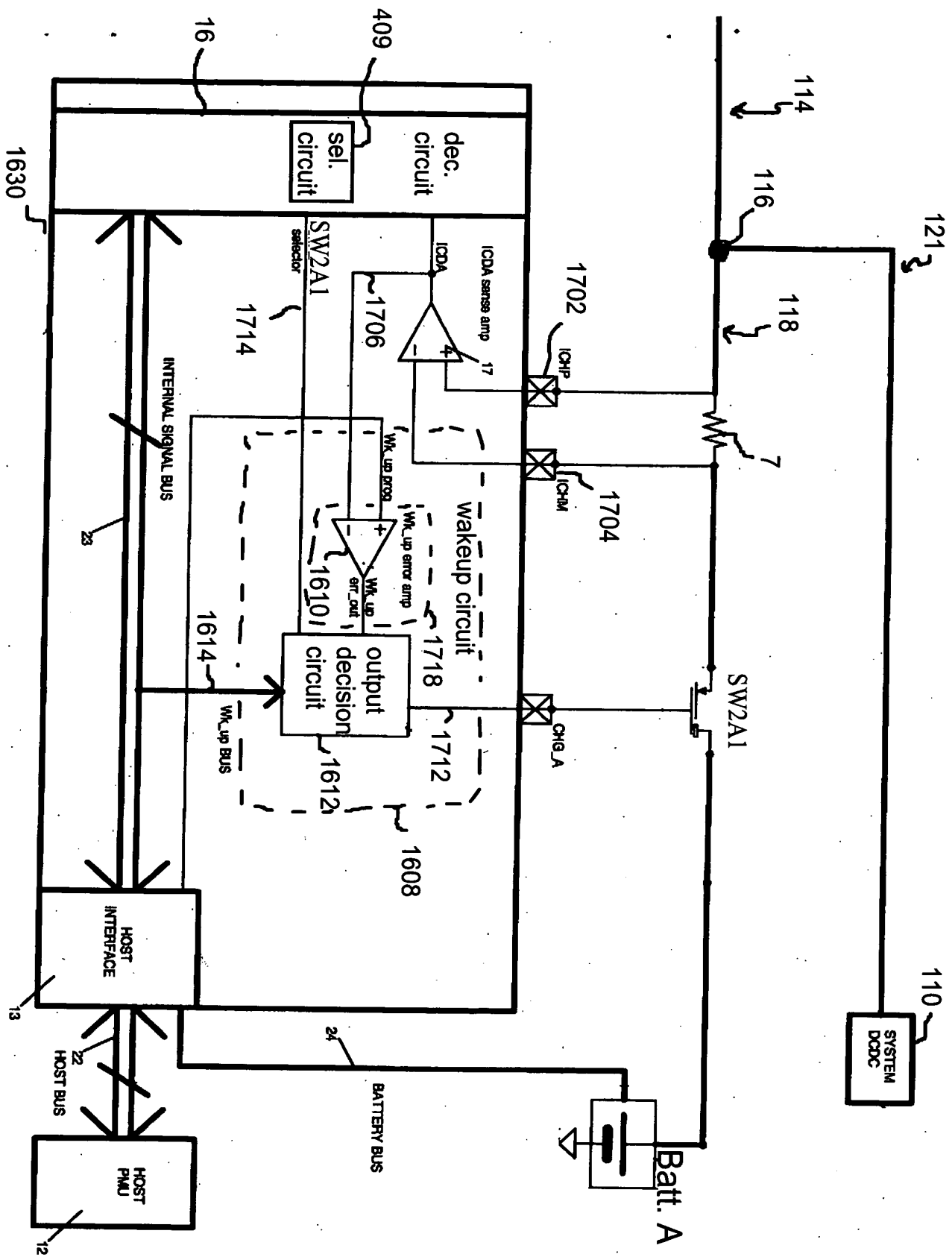


FIG. 17